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Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 4 , 25-28 May 2003

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#### 2 A novel self-tuning pulse width modulator based on master-slave architecture for a Class D amplifier

*Tan, M.T.; Chang, J.S.; Tong, Y.C.;*

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 2 , 30 May-2 June 1999

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#### 3 A novel self-error correction pulse width modulator for a class D amplifier for hearing instruments

*Tan, M.T.; Chang, J.S.; Cheng, Z.H.; Tong, Y.C.;*

Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on , Volume: 1 , 31 May-3 June 1998

Pages:261 - 264 vol.1

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#### 4 Analysis and design of power efficient class D amplifier output stage

*Chang, J.S.; Meng-Tong Tan; Zhihong Cheng; Yit-Chow Tong;*

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on

on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on] , Volume: 47 , Issue: 6 , June 2000  
Pages:897 - 902

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**5 Novel self-error-correction pulse width modulator for a class D hear instrument amplifier**

*Tan, M.; Chang, J.S.; Cheng, Z.; Tong, T.C.;*

Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G-Circuits, Devices and Systems] , Volume: 148 , Issue: 5 , Oct. 2001  
Pages:261 - 266

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